

T for 1k & 10n is 10us
T for 10k & 10n is 100us

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 Design by Martin van den Nieuwelaar, martin@gadgets.co.nz

For input, influence, or ideas, special thanks to:

Matt Fox
 Daniel Jury
 Andrew Laphorn
 Bart Wenmeckers
 Ian Williamson

I/O connectors and filters

TITLE: inverter-logic-v101

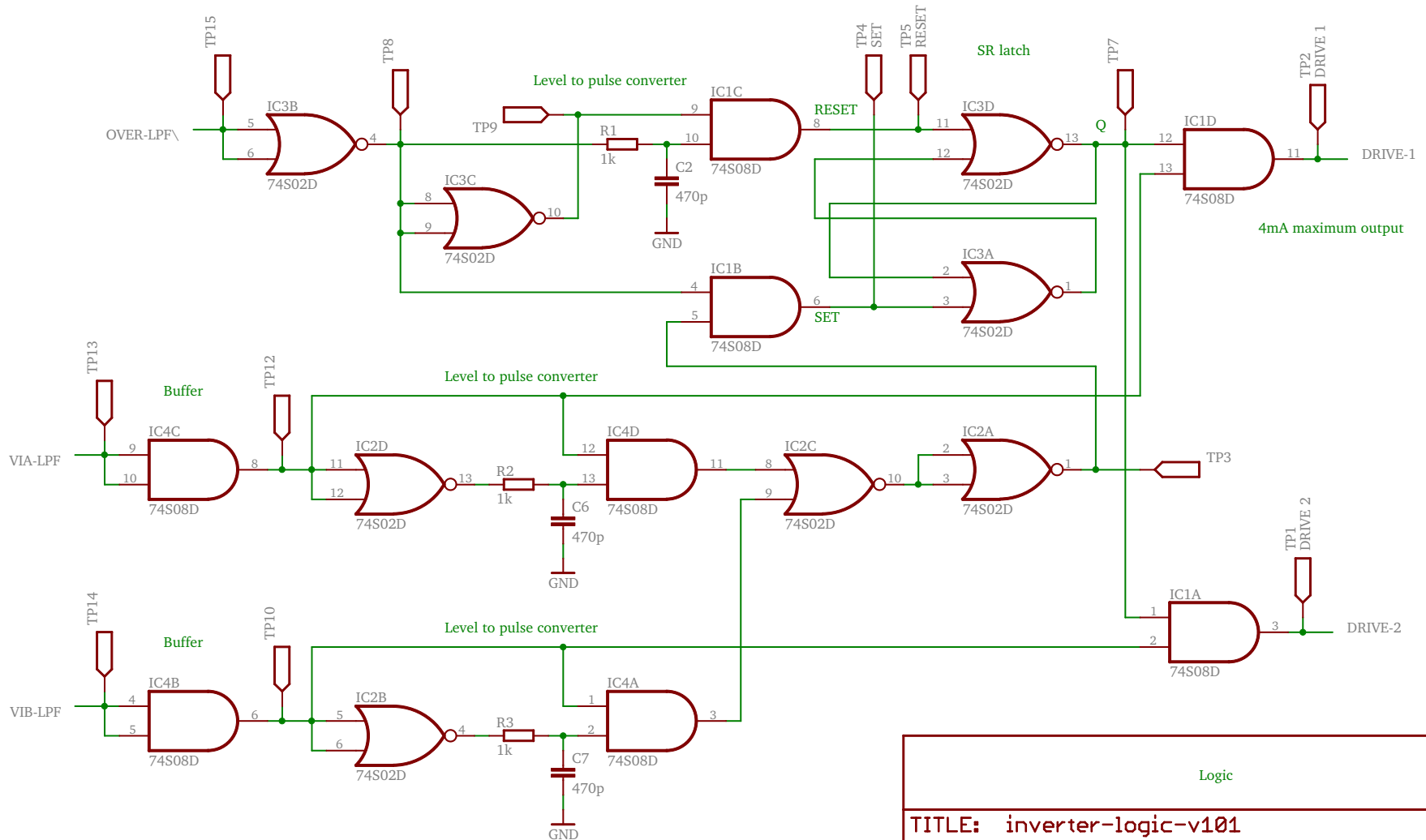
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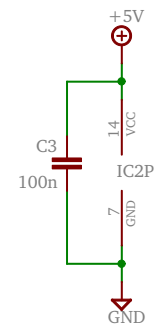
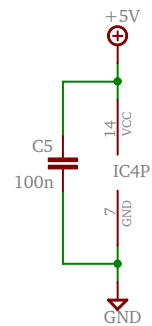
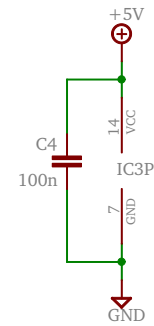
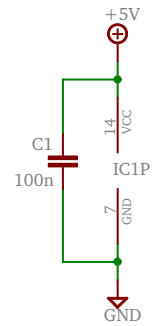
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74HCT02 quad NOR gate, NXP 74HCT02D 14-SOIC 3.9mm width, Digkey #568-1500-1-ND
 74HCT08 quad 2-way AND gate, Diodes Inc. 74HCT08S14-13, Digkey #74HCT08S14-13DICT-ND



Logic	
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Gate power supplies

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